Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	345	(design with variation) and (process with variation) and (simulat\$4 model\$4) OR		OR	OFF .	2005/11/30 11:31
S2	12	(design adj variation) and (process adj variation) and (simulat\$4 model\$4)	US-PGPUB; USPAT	OR	OFF	2005/11/30 11:33
S3	162	(design adj var\$8) and (process adj var\$8) and (simulat\$4 model\$4)	US-PGPUB; USPAT	OR	OFF	2005/11/30 11:57
S9	4409	(performance adj parameter) and (simulat\$4 model\$4)	US-PGPUB; USPAT	OR	ON	2005/11/30 12:01
S8	18195	(performance with parameter) and (simulat\$4 model\$4)	US-PGPUB; USPAT	OR	ON	2005/11/30 12:01
S12	45	("5646870").URPN.	USPAT	OR	OFF	2005/11/30 12:03
S13	7	process with design with correlat\$4 with parameter	USPAT	OR	OFF	2005/11/30 15:20
S16	22	S15 not S14	US-PGPUB; USPAT	OR	ON	2005/11/30 15:22
S15	35	process with design with correlat\$4 with parameter	US-PGPUB; USPAT	OR	ON	2005/11/30 15:22
S14	13	process with design with correlat\$4 with parameter	US-PGPUB; USPAT	OR	OFF	2005/11/30 15:22
S17	0	"10/904307"	US-PGPUB	OR	ON	2005/11/30 17:16
S26	19	"RICHARD WILLIAMS".in.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/11/30 17:20
S25	0	"RICHARD Q. WILLIAMS".in.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/11/30 17:20
S24	0	"WILLIAMS, RICHARD Q.".in.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/11/30 17:20
S23	0	"WILLIAMS RICHARD Q.".in.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/11/30 17:20
S22	0	"WILLIAMS; RICHARD Q.".in.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/11/30 17:20
S21	0	"WILLIAMS; RICHARD Q.".in.	USPAT	OR	OFF	2005/11/30 17:20
S18	3	"6329690"	USPAT	OR	OFF	2005/11/30 17:24
S20	1	"6314390 ".pn.	USPAT	OR	OFF	2005/11/30 17:25
S19	1	"6329690".pn.	USPAT	OR	OFF	2005/11/30 17:25
S27	4409	(performance adj parameter) and (simulat\$4 model\$4)	US-PGPUB; USPAT	OR	ON	2005/11/30 18:11

S11	1659	S9 and (performance with (rang\$4 bound limit\$4))	US-PGPUB; USPAT	OR	ON	2005/11/30 18:11
S28	1659	S27 and (performance with (rang\$4 bound limit\$4))	US-PGPUB; USPAT	OR	ON	2005/11/30 18:12
S31	14	BSIM and (DEPICT SUPREM)	US-PGPUB; USPAT	OR	ON	2005/11/30 18:13
S30	350	S27 and (device with (simulat\$4 model\$\$)) and (process with (simulat\$4 model\$4))	US-PGPUB; USPAT	OR	ON	2005/11/30 18:20
S32	1417	(performance adj parameter) with (range bound limit\$ condition)	US-PGPUB; USPAT	OR	ON	2005/11/30 18:21
S34	2	S32 and (device adj model\$4) and (process\$4 adj model\$4)	US-PGPUB; USPAT	OR	ON	2005/11/30 18:22
S35	5	S32 and (device adj (model\$4 simulat\$4)) and (process\$4 adj (model\$4 simulat\$))	US-PGPUB; USPAT	OR	ON	2005/11/30 18:23
S33	674	S32 and (simulat\$4 model\$)	US-PGPUB; USPAT	OR	ON	2005/11/30 18:25
S39	229	S33 and (semiconductor IC (integrated adj circuit))	US-PGPUB; USPAT	OR	ON	2005/11/30 18:26
S38	201	S33 and (semiconductor (integrated adj circuit))	US-PGPUB; USPAT	OR	ON	2005/11/30 18:26
S37	352	S33 and (semiconductor integrated)	US-PGPUB; USPAT	OR	ON	2005/11/30 18:26
S41	2	"10/023235"	US-PGPUB	OR	OFF	2005/12/01 11:39
S40	7	("6028994").URPN.	USPAT	OR	OFF	2005/12/01 11:39
S43	792	703/13.ccls.	US-PGPUB; USPAT	OR	OFF	2005/12/01 17:19
S46	48	("5860032" "5884050" "5921896" "5931136" "5968153" "5986845" "5991043" "6007203" "6029000" "6070992" "6084386" "6111832" "6115772" "6142410" "6161161" "6173015" "6208284" "6218274" "6219787" "6218274" "6219787" "6243610" "6247069" "6266731" "6344732" "6346807" "6396137" "6419636" "6467282" "6626144" "6721859" "6727977" "6775605" "6819991" "6825631" "6944704" "5978860" "5007736" "5279276" "5540821" "5984475" "6088095" "6088038" "6188786" "4244583" "4318330" "4320703" "4345252" "4347679" "4367934").pn.	US-PGPUB; USPAT	OR	OFF	2005/12/01 17:28

S45	50	("5487167" "4630845" "5540216" "5576973" "5727125" "4265720" "4295528" "4342032" "4445968" "4470351" "4477079" "4533918" "4782223" "4784523" "4817954" "4852153" "4857987" "4891597" "4937437" "4953443" "4982621" "4985887" "5179700" "5191411" "5204951" "5214653" "5223661" "5249500" "5257368" "5223661" "5249500" "5257368" "5261970" "5267179" "5405108" "5423048" "5442376" "5485139" "549378" "5521464" "5521579" "5566317" "5575072" "5581594" "5602959" "579565189" "5794186" "5798518" "5765189" "5794186" "5798518" "5794186" "5798518" "5811721" "5832130").pn.	US-PGPUB; USPAT	OR	OFF	2005/12/01 17:28
S44	15	(US-20020059339-\$).did. or (US-5282128-\$ or US-5319564-\$ or US-5646870-\$ or US-5655110-\$ or US-5819208-\$ or US-5966527-\$ or US-6028994-\$ or US-6249712-\$ or US-6269277-\$ or US-6298470-\$ or US-6311096-\$ or US-6407611-\$ or US-6598210-\$ or US-6795952-\$). did.	US-PGPUB; USPAT	OR	OFF	2005/12/01 17:28



Home | Login | Logout | Access Information | Alerts |

Welcome United States Patent and Trademark Office

☐ Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

Results for "	((posynomial) <in>metadata)"</in>
Vour search	matched 25 of 1263585 documents

⊠e-mail

» Search Options		Modify Search				
View Session History		((pos	/nomial) <in>metadata)</in>			
New Search		Гс	heck to search only within this results set			
		Disp	lay Format: Citation C Citation & Abstract			
» Key						
IEEE JNL	IEEE Journal or Magazine	Select	Article Information			
IEE JNL	IEE Journal or Magazine		1. Simulation-based automatic generation of signomial and posynomial per			
IEEE CNF	IEEE Conference Proceeding	•	models for analog integrated circuit sizing Daems, W.; Gielen, G.; Sansen, W.;			
IEE CNF	IEE Conference Proceeding		Computer Aided Design, 2001. ICCAD 2001. IEEE/ACM International Conference 4-8 Nov. 2001 Page(s):70 - 74			
IEEE STD	IEEE Standard		Digital Object Identifier 10.1109/ICCAD.2001.968600			
			AbstractPlus Full Text: PDF(500 KB) IEEE CNF			
		□	 Robust analog/RF circuit design with projection-based posynomial mode Xin Li; Gopalakrishnan, P.; Yang Xu; Pileggi, T.; Computer Aided Design, 2004. ICCAD-2004. IEEE/ACM International Conference 7-11 Nov. 2004 Page(s):855 - 862 			
			AbstractPlus Full Text: PDF(884 KB) IEEE CNF			
		ū	 Generalized posynomial performance modeling [analog ICs] Eeckelaert, T.; Daems, W.; Gielen, G.; Sansen, W.; Design, Automation and Test in Europe Conference and Exhibition, 2003 2003 Page(s):250 - 255 Digital Object Identifier 10.1109/DATE.2003.1186394 			
			AbstractPlus Full Text: PDF(320 KB) IEEE CNF			
		Г	4. An efficient optimization-based technique to generate posynomial performand for analog integrated circuits Daems, W.; Gielen, G.; Sansen, W.; Design Automation Conference, 2002. Proceedings. 39th 10-14 June 2002 Page(s):431 - 436 Digital Object Identifier 10.1109/DAC.2002.1012664			
	,		AbstractPlus Full Text: PDF(704 KB) IEEE CNF			
		C	 Simulation-based generation of posynomial performance models for the sintegrated circuits Daems, W.; Gielen, G.; Sansen, W.; Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions Volume 22, Issue 5, May 2003 Page(s):517 - 534 Digital Object Identifier 10.1109/TCAD.2003.810742 			
			AbstractPlus References Full Text: PDF(1630 KB) IEEE JNL			
			6. Layout optimization using arbitrarily high degree posynomial models			

	Sancheti, P.K.; Sapatnekar, S.S.; Circuits and Systems, 1995. ISCAS '95., 1995 IEEE International Symposium of Volume 1, 28 April-3 May 1995 Page(s):53 - 56 vol.1 Digital Object Identifier 10.1109/ISCAS.1995.521449
	AbstractPlus Full Text: PDF(492 KB) IEEE CNF
	 Primal algorithm of fuzzy posynomial geometric programming Cao Bing-yuan; IFSA World Congress and 20th NAFIPS International Conference, 2001. Joint Volume 1, 25-28 July 2001 Page(s):31 - 34 vol.1 Digital Object Identifier 10.1109/NAFIPS.2001.944222 AbstractPlus Full Text: PDF(184 KB) IEEE CNF
	8. An efficient approach to simultaneous transistor and interconnect sizing Jason Cong; Lei He; Computer-Aided Design, 1996. ICCAD-96. Digest of Technical Papers., 1996 I International Conference on 10-14 Nov. 1996 Page(s):181 - 186 Digital Object Identifier 10.1109/ICCAD.1996.569580
	AbstractPlus Full Text: PDF(652 KB) IEEE CNF
	 Optimal design of macrocells for low power and high speed Sancheti, P.K.; Sapatnekar, S.S.; Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction: Volume 15, Issue 9, Sept. 1996 Page(s):1160 - 1166 Digital Object Identifier 10.1109/43.536722
	AbstractPlus References Full Text: PDF(764 KB) IEEE JNL
	10. Multi-objective geometric programming with T-fuzzy variables Cao Bing-Yuan; North American Fuzzy Information Processing Society, NAFIPS 2003, 22nd In Conference of the 24-26 July 2003 Page(s):456 - 461 Digital Object Identifier 10.1109/NAFIPS.2003.1226828
	AbstractPlus Full Text: PDF(470 KB) IEEE CNF
	11. Optimal design of a CMOS op-amp via geometric programming Hershenson, M.delM.; Boyd, S.P.; Lee, T.H.; Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction Volume 20, Issue 1, Jan. 2001 Page(s):1 - 21 Digital Object Identifier 10.1109/43.905671
	AbstractPlus References Full Text: PDF(392 KB) IEEE JNL
	12. Timing modeling and optimization under the transmission line model Tai-Chen Chen; Song-Ra Pan; Yao-Wen Chang; Very Large Scale Integration (VLSI) Systems, IEEE Transactions on Volume 12, Issue 1, Jan. 2004 Page(s):28 - 41 Digital Object Identifier 10.1109/TVLSI.2003.820529
	AbstractPlus References Full Text: PDF(520 KB) IEEE JNL
	13. Robust gate sizing by geometric programming Singh, J.; Nookala, V.; Zhi-Quan Luo; Sapatnekar, S.; Design Automation Conference, 2005. Proceedings. 42nd 13-17 June 2005 Page(s):315 - 320
	AbstractPlus Full Text: PDF(391 KB) IEEE CNF
\Box	14. Performance modeling of analog integrated circuits using least-squares

Kiely, T.; Gielen, G.; Design, Automation and Test in Europe Conference and Exhibition, 2004. Proc Volume 1, 16-20 Feb. 2004 Page(s):448 - 453 Vol.1 Digital Object Identifier 10.1109/DATE.2004.1268887 AbstractPlus | Full Text: PDF(266 KB) IEEE CNF 15. Geometric programming based power-delay optimization using transisto Г submicron and deep submicron CMOS inverter Pattanaik, M.; Banerjee, S.; TENCON 2003. Conference on Convergent Technologies for Asia-Pacific Reg Volume 1, 15-17 Oct. 2003 Page(s):484 - 487 Vol.1 Digital Object Identifier 10.1109/TENCON.2003.1273369 AbstractPlus | Full Text: PDF(307 KB) | IEEE CNF 16. Design and optimization of LC oscillators Hershenson, M.D.M.; Hajimiri, A.; Mohan, S.S.; Boyd, S.P.; Lee, T.H.; Computer-Aided Design, 1999. Digest of Technical Papers. 1999 IEEE/ACM II Conference on 7-11 Nov. 1999 Page(s):65 - 69 Digital Object Identifier 10.1109/ICCAD.1999.810623 AbstractPlus | Full Text: PDF(372 KB) | IEEE CNF 17. Automated design of folded-cascode op-amps with sensitivity analysis del Mar Hershenson, M.; Boyd, S.P.; Lee, T.H.; Electronics, Circuits and Systems, 1998 IEEE International Conference on Volume 1, 7-10 Sept. 1998 Page(s):121 - 124 vol.1 Digital Object Identifier 10.1109/ICECS.1998.813284 AbstractPlus | Full Text: PDF(304 KB) IEEE CNF 18. GPCAD: a tool for CMOS op-amp synthesis del Mar Hershenson, M.; Boyd, S.P.; Lee, T.H.; Computer-Aided Design, 1998. ICCAD 98. Digest of Technical Papers. 1998 II International Conference on 8-12 Nov 1998 Page(s):296 - 303 AbstractPlus | Full Text: PDF(704 KB) IEEE CNF 19. EWA: efficient wiring-sizing algorithm for signal nets and clock nets Kay, R.; Pileggi, L.T.; Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction Volume 17, Issue 1, Jan. 1998 Page(s):40 - 49 Digital Object Identifier 10.1109/43.673631 AbstractPlus | References | Full Text: PDF(208 KB) | IEEE JNL 20. CMOS op-amp sizing using a geometric programming formulation Mandal, P.; Visvanathan, V.; Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction Volume 20, Issue 1, Jan. 2001 Page(s):22 - 38 Digital Object Identifier 10.1109/43.905672 AbstractPlus | References | Full Text: PDF(360 KB) | IEEE JNL 21. Analysis of simulation-driven numerical performance modeling technique П application to analog circuit optimization McConaghy, T.; Gielen, G.; Circuits and Systems, 2005. ISCAS 2005. IEEE International Symposium on 23-26 May 2005 Page(s):1298 - 1301 Vol. 2 Digital Object Identifier 10.1109/ISCAS.2005.1464833 AbstractPlus | Full Text: PDF(184 KB) | IEEE CNF

	22. Generalized posynomial performance modeling Eeckelaert, T.; Daems, W.; Gielen, G.; Sansen, W.; Design, Automation and Test in Europe Conference and Exhibition, 2003 2003 Page(s):250 - 255 Digital Object Identifier 10.1109/DATE.2003.1253616 AbstractPlus Full Text: PDF(320 KB) IEEE CNF
	23. Performance optimization by wire and buffer sizing under the transmissic Tai-Chen Chen; Song-Ra Pan; Yao-Wen Chang; Computer Design, 2001. ICCD 2001. Proceedings. 2001 International Confere 23-26 Sept. 2001 Page(s):192 - 198 Digital Object Identifier 10.1109/ICCD.2001.955024
	AbstractPlus Full Text: PDF(616 KB) IEEE CNF
	24. Optimal delay-power tradeoffs for replicative logic circuitry Eisele, V.; Schmitt-Landsiedel, D.; Circuits and Systems, 1991., IEEE International Sympoisum on 11-14 June 1991 Page(s):2264 - 2267 vol.4 Digital Object Identifier 10.1109/ISCAS.1991.176831
	AbstractPlus Full Text: PDF(268 KB) IEEE CNF
Π	25. A fitting approach to generate symbolic expressions for linear and nonlir circuit performance characteristics Daems, W.; Gielen, G.; Sansen, W.; Design, Automation and Test in Europe Conference and Exhibition, 2002. Prod 4-8 March 2002 Page(s):268 - 273 Digital Object Identifier 10.1109/DATE.2002.998283 AbstractPlus Full Text: PDF(336 KB) IEEE CNF

Indexed by

Help Contact Us Privacy &

© Copyright 2005 IEEE -



Home | Login | Logout | Access Information | Alerts |

Welcome United States Patent and Trademark Office

☐ Search Results **BROWSE SEARCH IEEE XPLORE GUIDE**

Results for "(geometric programming<in>metadata)" Your search matched 94 of 1263585 documents.

⊠e-mail

A maximum of 100 results are displayed,	100 to a page,	sorted by F	ublication y	ear in Asceı	nding order.

		Modify Search
		(geometric programming <in>metadata)</in>
» Search O	ptions	Check to search only within this results set
View Sessi	on History	Display Format: Citation C Citation & Abstract
New Searc	<u>h</u>	Select Article Information
» Key		1. A computer-aided approach for Routh-Pade approximants
IEEE JNL	IEEE Journal or Magazine	Chandra, D.; Singh, V.; Circuits and Systems, 1989., Proceedings of the 32nd Midwest Symposium of 14-16 Aug. 1989 Page(s):402 - 405 vol.1
IEE JNL	IEE Journal or Magazine	Digital Object Identifier 10.1109/MWSCAS.1989.101876
IEEE CNF	IEEE Conference Proceeding	AbstractPlus Full Text: PDF(228 KB) IEEE CNF
IEE CNF	IEE Conference Proceeding	2. A cost minimization model for satellites offering customer premise services.
IEEE STD	IEEE Standard	Dutta, A.; Rama, D.V.; Telecommunications Symposium, 1990. ITS '90 Symposium Record., SBT/II 3-6 Sept. 1990 Page(s):399 - 403 Digital Object Identifier 10.1109/ITS.1990.175635
		AbstractPlus Full Text: PDF(400 KB) IEEE CNF
		3. 3-D structure modeling via constraint resolution Tyan, HR.; Jie-Yong Juang; Tools for Artificial Intelligence, 1991. TAI '91., Third International Conference 10-13 Nov. 1991 Page(s):274 - 281 Digital Object Identifier 10.1109/TAI.1991.167105
		AbstractPlus Full Text: PDF(624 KB) IEEE CNF
		4. An ATMS-based geometric constraint solver for 3D CAD Shimizu, S.; Inoue, K.; Numao, M.; Tools for Artificial Intelligence, 1991. TAI '91., Third International Conference 10-13 Nov. 1991 Page(s):282 - 290 Digital Object Identifier 10.1109/TAI.1991.167106 AbstractPlus Full Text: PDF(648 KB) IEEE CNF
		Austracti ius 1 uii fext. <u>i Dr (</u> 040 ND) IEEE ON
		5. Algorithmic support for intelligent fire-code checking Delis, E.A.; Delis, A.; Tools for Artificial Intelligence, 1991. TAI '91., Third International Conference 10-13 Nov. 1991 Page(s):291 - 298 Digital Object Identifier 10.1109/TAI.1991.167107
		AbstractPlus Full Text: PDF(616 KB) IEEE CNF
		6. An optimization model of communications satellite planning Dutta, A.; Rama, D.V.;

Communications, IEEE Transactions on Volume 40, Issue 9, Sept. 1992 Page(s):1463 - 1473 Digital Object Identifier 10.1109/26.163567 AbstractPlus | Full Text: PDF(844 KB) | IEEE JNL 7. Distributed real-time control of a spatial robot juggler Rizzi, A.A.; Whitcomb, L.L.; Koditschek, D.E.; Computer Volume 25, Issue 5, May 1992 Page(s):12 - 24 Digital Object Identifier 10.1109/2.144392 AbstractPlus | Full Text: PDF(1260 KB) | IEEE JNL 8. Geometric parametrization and constrained optimization techniques in th salient pole synchronous machines Weeber, K.; Hoole, S.R.H.; Magnetics, IEEE Transactions on Volume 28, Issue 4, July 1992 Page(s):1948 - 1960 Digital Object Identifier 10.1109/20.144753 AbstractPlus | Full Text: PDF(1132 KB) | IEEE JNL 9. An object-oriented approach to template guided visual inspection Mundy, J.; Noble, A.; Marinos, C.; Nguyen, V.D.; Heller, A.; Farley, J.; Tran, A. Computer Vision and Pattern Recognition, 1992. Proceedings CVPR '92., 1992 Society Conference on 15-18 June 1992 Page(s):386 - 392 Digital Object Identifier 10.1109/CVPR.1992.223160 AbstractPlus | Full Text: PDF(560 KB) IEEE CNF 10. A priority based service algorithm for use in time-critical and integrated \$ П networks Husein, S.; Woodward, M.E.; Szumko, S.; Networks, 1993. International Conference on Information Engineering '93. 'Cor Networks for the Year 2000', Proceedings of IEEE Singapore International Cor Volume 1, 6-11 Sept. 1993 Page(s):93 - 97 vol.1 Digital Object Identifier 10.1109/SICON.1993.515734 AbstractPlus | Full Text: PDF(364 KB) | IEEE CNF 11. Chance design costs-a distributional limit Stark, R.M.; Machida, M.; Uncertainty Modeling and Analysis, 1993. Proceedings., Second International 25-28 April 1993 Page(s):269 - 270 Digital Object Identifier 10.1109/ISUMA.1993.366758 AbstractPlus | Full Text: PDF(144 KB) | IEEE CNF 12. A Bayesian analysis of entropy optimization for uncertainty modeling \Box Fang, S.C.; Lee, D.N.; Tsao, H.-S.J.; Uncertainty Modeling and Analysis, 1993. Proceedings., Second International 25-28 April 1993 Page(s):408 - 414 Digital Object Identifier 10.1109/ISUMA.1993.366737 AbstractPlus | Full Text: PDF(480 KB) | IEEE CNF 13. An analog network for geometric optimization problem Li, X.; Wong, W.S.; Neural Networks, 1994. IEEE World Congress on Computational Intelligence., International Conference on Volume 4, 27 June-2 July 1994 Page(s):2153 - 2158 vol.4 Digital Object Identifier 10.1109/ICNN.1994.374549 AbstractPlus | Full Text: PDF(284 KB) | IEEE CNF

	 The localization problem for mobile robots Kleinberg, J.M.; Foundations of Computer Science, 1994 Proceedings., 35th Annual Symposiu 20-22 Nov. 1994 Page(s):521 - 531 Digital Object Identifier 10.1109/SFCS.1994.365739 AbstractPlus Full Text: PDF(832 KB) IEEE CNF
	15. Fuzzy geometric programming optimum seeking of scheme for waste-wa power plant Cao Bing-Yuan; Fuzzy Systems, 1995. International Joint Conference of the Fourth IEEE Interr Conference on Fuzzy Systems and The Second International Fuzzy Engineeri Proceedings of 1995 IEEE International Conference on Volume 2, 20-24 March 1995 Page(s):793 - 798 vol.2 Digital Object Identifier 10.1109/FUZZY.1995.409773 AbstractPlus Full Text: PDF(212 KB) IEEE CNF
	16. Improved algorithms in geometric optimization via expanders Katz, M.J.; Theory of Computing and Systems, 1995. Proceedings., Third Israel Symposit 4-6 Jan. 1995 Page(s):78 - 87 Digital Object Identifier 10.1109/ISTCS.1995.377043 AbstractPlus Full Text: PDF(784 KB) IEEE CNF
	17. Globally optimal floorplanning for a layout problem Teng-Sheng Moh; Tsu-Shuan Chang; Hakimi, S.L.; Circuits and Systems I: Fundamental Theory and Applications, IEEE Transacti Circuits and Systems I: Regular Papers, IEEE Transactions on] Volume 43, Issue 9, Sept. 1996 Page(s):713 - 720 Digital Object Identifier 10.1109/81.536741 AbstractPlus References Full Text: PDF(760 KB) IEEE JNL
	18. Genetic programming techniques that evolve recurrent neural network as signal processing Esparcia-Alcazar, A.I.; Sharman, K.C.; Neural Networks for Signal Processing [1996] VI. Proceedings of the 1996 IEE Processing Society Workshop 4-6 Sept. 1996 Page(s):139 - 148 Digital Object Identifier 10.1109/NNSP.1996.548344 AbstractPlus Full Text: PDF(416 KB) IEEE CNF
	19. Geometric computation based on an adaptive data length computation policies, M.; Yoshida, M.; Kitamura, T.; Yamaguchi, F.; Computer Graphics International, 1996. Proceedings 24-28 June 1996 Page(s):122 - 130 Digital Object Identifier 10.1109/CGI.1996.511794 AbstractPlus Full Text: PDF(656 KB) IEEE CNF
匚	20. Optimal permutation codes for the Gaussian channel Karlof, J.K.; Chang, Y.O.; Information Theory, IEEE Transactions on Volume 43, Issue 1, Jan. 1997 Page(s):356 - 358 Digital Object Identifier 10.1109/18.567763 AbstractPlus References Full Text: PDF(328 KB) IEEE JNL
	21. Optimal estimation of interconnections of nonlinear interconnected subs Peltsverger, B.; Shtessel, Y.; System Theory, 1997., Proceedings of the Twenty-Ninth Southeastern Sympo:

Digital Object Identifier 10.1109/SSST.1997.581610 AbstractPlus | Full Text: PDF(344 KB) | IEEE CNF 22. Optimal wire and transistor sizing for circuits with non-tree topology Vandenberghe, L.; Boyd, S.; El Gamal, A.; Computer-Aided Design, 1997. Digest of Technical Papers., 1997 IEEE/ACM I Conference on 9-13 Nov. 1997 Page(s):252 - 259 Digital Object Identifier 10.1109/ICCAD.1997.643528 AbstractPlus | Full Text: PDF(700 KB) | IEEE CNF 23. GPCAD: a tool for CMOS op-amp synthesis del Mar Hershenson, M.; Boyd, S.P.; Lee, T.H.; Computer-Aided Design, 1998. ICCAD 98. Digest of Technical Papers. 1998 II International Conference on 8-12 Nov 1998 Page(s):296 - 303 AbstractPlus | Full Text: PDF(704 KB) IEEE CNF 24. Automated design of folded-cascode op-amps with sensitivity analysis del Mar Hershenson, M.; Boyd, S.P.; Lee, T.H.; Electronics, Circuits and Systems, 1998 IEEE International Conference on Volume 1, 7-10 Sept. 1998 Page(s):121 - 124 vol.1 Digital Object Identifier 10.1109/ICECS.1998.813284 AbstractPlus | Full Text: PDF(304 KB) IEEE CNF 25. Partitioning 3D surface meshes using watershed segmentation Mangan, A.P.; Whitaker, R.T.; Visualization and Computer Graphics, IEEE Transactions on Volume 5, Issue 4, Oct.-Dec. 1999 Page(s):308 - 321 Digital Object Identifier 10.1109/2945.817348 AbstractPlus | References | Full Text: PDF(856 KB) IEEE JNL 26. Design and optimization of LC oscillators \Box Hershenson, M.D.M.; Hajimiri, A.; Mohan, S.S.; Boyd, S.P.; Lee, T.H.; Computer-Aided Design, 1999. Digest of Technical Papers. 1999 IEEE/ACM II Conference on 7-11 Nov. 1999 Page(s):65 - 69 Digital Object Identifier 10.1109/ICCAD.1999.810623 AbstractPlus | Full Text: PDF(372 KB) IEEE CNF 27. Concurrent logic restructuring and placement for timing closure П Jinan Lou; Wei Chen; Pedram, M.; Computer-Aided Design, 1999. Digest of Technical Papers. 1999 IEEE/ACM II Conference on 7-11 Nov. 1999 Page(s):31 - 35 Digital Object Identifier 10.1109/ICCAD.1999.810615 AbstractPlus | Full Text: PDF(556 KB) | IEEE CNF 28. Fuzzy geometric programming optimum seeking in power supply radius substation Cao Bing-Yuan; Fuzzy Systems Conference Proceedings, 1999. FUZZ-IEEE '99. 1999 IEEE In Volume 3, 22-25 Aug. 1999 Page(s):1749 - 1753 vol.3 Digital Object Identifier 10.1109/FUZZY.1999.790171 AbstractPlus | Full Text: PDF(176 KB) | IEEE CNF

9-11 March 1997 Page(s):218 - 222

	Coleman, J.O.; Scholnik, D.P.; Circuits and Systems, 1999. 42nd Midwest Symposium on Volume 1, 8-11 Aug. 1999 Page(s):409 - 412 vol. 1 Digital Object Identifier 10.1109/MWSCAS.1999.867292
	AbstractPlus Full Text: PDF(380 KB) IEEE CNF
	30. Optimization of inductor circuits via geometric programming del Mar Hershenson, M.; Mohan, S.S.; Boyd, S.P.; Lee, T.H.; Design Automation Conference, 1999. Proceedings. 36th 21-25 June 1999 Page(s):994 - 998 Digital Object Identifier 10.1109/DAC.1999.782241 AbstractPlus Full Text: PDF(424 KB) IEEE CNF
	31. Bandwidth extension in CMOS with optimized on-chip inductors Mohan, S.S.; Hershenson, M.D.M.; Boyd, S.P.; Lee, T.H.; Solid-State Circuits, IEEE Journal of Volume 35, Issue 3, March 2000 Page(s):346 - 355 Digital Object Identifier 10.1109/4.826816 AbstractPlus References Full Text: PDF(352 KB) IEEE JNL
	32. Simultaneous gate sizing and placement Wei Chen; Cheng-Ta Hseih; Pedram, M.; Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction Volume 19, Issue 2, Feb. 2000 Page(s):206 - 214 Digital Object Identifier 10.1109/43.828549
	AbstractPlus References Full Text: PDF(232 KB) IEEE JNL
	33. Convex optimization of antenna power control for wireless cellular system Mung Chiang; Antennas and Propagation for Wireless Communications, 2000 IEEE-APS Con 6-8 Nov. 2000 Page(s):179 - 182 Digital Object Identifier 10.1109/APWC.2000.900171
	AbstractPlus Full Text: PDF(220 KB) IEEE CNF
	34. Optimal allocation of local feedback in multistage amplifiers via geometri Dawson, J.L.; Boyd, S.P.; Lee, T.H.; del Mar Hershenson, M.; Circuits and Systems, 2000. Proceedings of the 43rd IEEE Midwest Symposiu Volume 1, 8-11 Aug. 2000 Page(s):530 - 533 vol.1 Digital Object Identifier 10.1109/MWSCAS.2000.951700
	AbstractPlus Full Text: PDF(248 KB) IEEE CNF
C	35. Simultaneous rate and power control in multirate multimedia CDMA syste Kandukuri, S.; Boyd, S.; Spread Spectrum Techniques and Applications, 2000 IEEE Sixth International Volume 2, 6-8 Sept. 2000 Page(s):570 - 574 vol.2 Digital Object Identifier 10.1109/ISSSTA.2000.876498
	AbstractPlus Full Text: PDF(376 KB) IEEE CNF
	36. H ₂ -suboptimal stable stabilization by linear programming Ferreira, P.A.V.; Cabanas, A.I.D.; American Control Conference, 2000. Proceedings of the 2000 Volume 6, 28-30 June 2000 Page(s):4378 - 4382 vol.6 Digital Object Identifier 10.1109/ACC.2000.877049 <u>AbstractPlus</u> Full Text: <u>PDF</u> (384 KB) IEEE CNF
	37. B-spline free-form deformation of polygonal objects through fast function Jieqing Feng; Qunsheng Peng; Geometric Modeling and Processing 2000. Theory and Applications. Proceedings.

10-12 April 2000 Page(s):408 - 414 Digital Object Identifier 10.1109/GMAP.2000.838272 AbstractPlus | Full Text: PDF(260 KB) IEEE CNF 38. Optimal design of a CMOS op-amp via geometric programming Г Hershenson, M.delM.; Boyd, S.P.; Lee, T.H.; Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction Volume 20, Issue 1, Jan. 2001 Page(s):1 - 21 Digital Object Identifier 10.1109/43.905671 AbstractPlus | References | Full Text: PDF(392 KB) | IEEE JNL 39. Optimal allocation of local feedback in multistage amplifiers via geometri Dawson, J.L.; Boyd, S.P.; del Mar Hershenson, M.; Lee, T.H.; Circuits and Systems I: Fundamental Theory and Applications, IEEE Transacti Circuits and Systems I: Regular Papers, IEEE Transactions on Volume 48, Issue 1, Jan. 2001 Page(s):1 - 11 Digital Object Identifier 10.1109/81.903183 AbstractPlus | References | Full Text: PDF(184 KB) | IEEE JNL 40. CMOS op-amp sizing using a geometric programming formulation Mandal, P.; Visvanathan, V.; Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction Volume 20, Issue 1, Jan. 2001 Page(s):22 - 38 Digital Object Identifier 10.1109/43.905672 AbstractPlus | References | Full Text: PDF(360 KB) | IEEE JNL 41. Handling soft modules in general nonslicing floorplan using Lagrangian Young, F.Y.; Chu, C.C.N.; Luk, W.S.; Wong, Y.C.; Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction Volume 20, Issue 5, May 2001 Page(s):687 - 692 Digital Object Identifier 10.1109/43.920707 AbstractPlus | References | Full Text: PDF(208 KB) | IEEE JNL 42. Resource allocation for QoS provisioning in wireless ad hoc networks Mung Chiang; Oneill, D.; Julian, D.; Boyd, S.; Global Telecommunications Conference, 2001. GLOBECOM '01. IEEE Volume 5, 25-29 Nov. 2001 Page(s):2911 - 2915 vol.5 Digital Object Identifier 10.1109/GLOCOM.2001.965961 AbstractPlus | Full Text: PDF(129 KB) IEEE CNF 43. Robust adaptive approximation of implicit curves Lopes, H.; Oliveira, J.B.; De Figueiredo, L.H.; Computer Graphics and Image Processing, 2001 Proceedings of XIV Brazilian 15-18 Oct. 2001 Page(s):10 - 17 Digital Object Identifier 10.1109/SIBGRAPI.2001.963032 AbstractPlus | Full Text: PDF(754 KB) IEEE CNF 44. Nonmanifold subdivision Lexing Ying; Zorin, D.; Visualization, 2001. VIS '01. Proceedings 21-26 Oct. 2001 Page(s):325 - 569 AbstractPlus | Full Text: PDF(631 KB) IEEE CNF 45. Primal algorithm of fuzzy posynomial geometric programming Cao Bing-yuan; IFSA World Congress and 20th NAFIPS International Conference, 2001. Joint Volume 1, 25-28 July 2001 Page(s):31 - 34 vol.1 Digital Object Identifier 10.1109/NAFIPS.2001.944222

AbstractPlus | Full Text: PDF(184 KB) IEEE CNF

□	46. Simulation-based automatic generation of signomial and posynomial per models for analog integrated circuit sizing Daems, W.; Gielen, G.; Sansen, W.; Computer Aided Design, 2001. ICCAD 2001. IEEE/ACM International Confere 4-8 Nov. 2001 Page(s):70 - 74 Digital Object Identifier 10.1109/ICCAD.2001.968600 AbstractPlus Full Text: PDF(500 KB) IEEE CNF
	47. Optimal power control in interference-limited fading wireless channels w probability specifications Kandukuri, S.; Boyd, S.; Wireless Communications, IEEE Transactions on Volume 1, Issue 1, Jan. 2002 Page(s):46 - 55 Digital Object Identifier 10.1109/7693.975444 AbstractPlus References Full Text: PDF(173 KB) IEEE JNL
	48. An efficient optimization-based technique to generate posynomial perfor for analog integrated circuits Daems, W.; Gielen, G.; Sansen, W.; Design Automation Conference, 2002. Proceedings. 39th 10-14 June 2002 Page(s):431 - 436 Digital Object Identifier 10.1109/DAC.2002.1012664 AbstractPlus Full Text: PDF(704 KB) IEEE CNF
	49. Controlled Lagrangians and electromechanical systems Hamberg, J.; American Control Conference, 2002. Proceedings of the 2002 Volume 4, 8-10 May 2002 Page(s):2794 - 2799 vol.4 Digital Object Identifier 10.1109/ACC.2002.1025212 AbstractPlus Full Text: PDF(503 KB) IEEE CNF
U	50. Convex optimization of output link scheduling and active queue manager constrained packet switches Mung Chiang; Chan, B.L.F.; Boyd, S.; Communications, 2002. ICC 2002. IEEE International Conference on Volume 4, 28 April-2 May 2002 Page(s):2126 - 2130 vol.4 Digital Object Identifier 10.1109/ICC.2002.997223 AbstractPlus Full Text: PDF(227 KB) IEEE CNF
	51. A geometric approach to strategic conflict detection and resolution [ATC Geser, A.; Munoz, C.; Digital Avionics Systems Conference, 2002. Proceedings. The 21st Volume 1, 27-31 Oct. 2002 Page(s):6B1-1 - 6B1-11 vol.1 Digital Object Identifier 10.1109/DASC.2002.1067985 AbstractPlus Full Text: PDF(773 KB) IEEE CNF
	52. Non-convex states constrained system control algorithm via geometric to Zhang Juan; Chen Jie; Intelligent Control and Automation, 2002. Proceedings of the 4th World Congret Volume 4, 10-14 June 2002 Page(s):3210 - 3214 vol.4 Digital Object Identifier 10.1109/WCICA.2002.1020127 AbstractPlus Full Text: PDF(421 KB) IEEE CNF
	53. A fitting approach to generate symbolic expressions for linear and nonlir circuit performance characteristics Daems, W.; Gielen, G.; Sansen, W.;

Design, Automation and Test in Europe Conference and Exhibition, 2002. Proc 4-8 March 2002 Page(s):268 - 273 Digital Object Identifier 10.1109/DATE.2002.998283 AbstractPlus | Full Text: PDF(336 KB) IEEE CNF 54. Automated optimal design of switched-capacitor filters Hassibi, A.; Hershenson, M.; Design, Automation and Test in Europe Conference and Exhibition, 2002. Proc 4-8 March 2002 Page(s):1111 Digital Object Identifier 10.1109/DATE.2002.998499 AbstractPlus | Full Text: PDF(222 KB) IEEE CNF 55. Design of pipeline analog-to-digital converters via geometric programmir del Mar Hershenson, M.; Computer Aided Design, 2002. ICCAD 2002. IEEE/ACM International Confere 10-14 Nov. 2002 Page(s):317 - 324 Digital Object Identifier 10.1109/ICCAD.2002.1167553 AbstractPlus | Full Text: PDF(540 KB) | IEEE CNF 56. Comments on "Handling soft modules in general nonslicing floorplan us relaxation" Teng-Sheng Moh; Tsu-Shuan Chang; Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction Volume 22, Issue 12, Dec 2003 Page(s):1684 - 1686 Digital Object Identifier 10.1109/TCAD.2003.819428 AbstractPlus | Full Text: PDF(234 KB) | IEEE JNL 57. Simulation-based generation of posynomial performance models for the П integrated circuits Daems, W.; Gielen, G.; Sansen, W.; Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction Volume 22, Issue 5, May 2003 Page(s):517 - 534 Digital Object Identifier 10.1109/TCAD.2003.810742 AbstractPlus | References | Full Text: PDF(1630 KB) | IEEE JNL 58. Observability and local observer construction for unknown parameters is nonlinearly parameterized systems Del Vecchio, D.; Murray, R.M.; American Control Conference, 2003. Proceedings of the 2003 Volume 6, 4-6 June 2003 Page(s):4748 - 4753 vol.6 Digital Object Identifier 10.1109/ACC.2003.1242473 AbstractPlus | Full Text: PDF(414 KB) | IEEE CNF 59. Convergence analysis and experimental study of geometric algorithms for grasping force optimization Liu, G.F.; Xu, J.J.; Li, Z.X.; Robotics and Automation, 2003. Proceedings. ICRA '03. IEEE International Co Volume 2, 14-19 Sept. 2003 Page(s):2683 - 2688 vol.2 Digital Object Identifier 10.1109/ROBOT.2003.1241998 AbstractPlus | Full Text: PDF(422 KB) | IEEE CNF 60. Viewpoint selection for object reconstruction using only local geometric \Box Jonnalagadda, K.; Lumia, R.; Starr, G.; Wood, J.; Robotics and Automation, 2003. Proceedings. ICRA '03. IEEE International Co Volume 2, 14-19 Sept. 2003 Page(s):2116 - 2122 vol.2 AbstractPlus | Full Text: PDF(543 KB) IEEE CNF ^{61.} Producing high-quality visualizations of large-scale simulation

Г	Popescu, V.; Hoffmann, C.; Kilic, S.; Sozen, M.; Meador, S.; Visualization, 2003. VIS 2003. IEEE 19-24 Oct. 2003 Page(s):575 - 580
	AbstractPlus Full Text: PDF(676 KB) IEEE CNF
	62. Multi-objective geometric programming with T-fuzzy variables Cao Bing-Yuan; North American Fuzzy Information Processing Society, NAFIPS 2003, 22nd In Conference of the 24-26 July 2003 Page(s):456 - 461 Digital Object Identifier 10.1109/NAFIPS.2003.1226828
	AbstractPlus Full Text: PDF(470 KB) IEEE CNF
□	63. Geometric programming based power-delay optimization using transisto submicron and deep submicron CMOS inverter Pattanaik, M.; Banerjee, S.; TENCON 2003. Conference on Convergent Technologies for Asia-Pacific Reg Volume 1, 15-17 Oct. 2003 Page(s):484 - 487 Vol.1 Digital Object Identifier 10.1109/TENCON.2003.1273369
	AbstractPlus Full Text: PDF(307 KB) IEEE CNF
Ü	64. GP based transistor sizing for optimal design of nanoscale CMOS inverted Pattanaik, M.; Banerjee, S.; Bahinipati, B.K.; Nanotechnology, 2003. IEEE-NANO 2003. 2003 Third IEEE Conference on Volume 2, 12-14 Aug. 2003 Page(s):524 - 527 vol. 2 Digital Object Identifier 10.1109/NANO.2003.1230962
	AbstractPlus Full Text: PDF(321 KB) IEEE CNF
	65. Rate distortion with state information: bounds and interpretations Chiang, M.; Information Theory, 2003. Proceedings. IEEE International Symposium on 29 June-4 July 2003 Page(s):147 Digital Object Identifier 10.1109/ISIT.2003.1228161 AbstractPlus Full Text: PDF(217 KB) IEEE CNF
	Austractrius Pull Text. <u>FDT (</u> 217 NB) TEEE GNP
	66. Evaluation of fully-integrated switching regulators for CMOS process tec Lee, J.; Hatcher, G.; Vandenbergh, L.; Chih-Kong Ken Yang; System-on-Chip, 2003. Proceedings. International Symposium on 19-21 Nov. 2003 Page(s):155 - 158 Digital Object Identifier 10.1109/ISSOC.2003.1267744
	AbstractPlus Full Text: PDF(347 KB) IEEE CNF
	67. Realizable parasitic reduction using generalized Y-/spl Delta/ transformat Zhanhai Qin; Chung-Kuan Cheng; Design Automation Conference, 2003. Proceedings 2-6 June 2003 Page(s):220 - 225
	AbstractPlus Full Text: PDF(683 KB) IEEE CNF
	68. Efficient optimization of constrained nonlinear resource allocation Chiang, M.; Sutivong, A.; Global Telecommunications Conference, 2003. GLOBECOM '03. IEEE Volume 7, 1-5 Dec. 2003 Page(s):3782 - 3786 vol.7 Digital Object Identifier 10.1109/GLOCOM.2003.1258939
	AbstractPlus Full Text: PDF(268 KB) IEEE CNF
П	69. Geometric programming dual of channel capacity Mung Chiang; Boyd, S.; Information Theory, 2003. Proceedings. IEEE International Symposium on

29 June-4 July 2003 Page(s):291 - 291 Digital Object Identifier 10.1109/ISIT.2003.1228306 AbstractPlus | Full Text: PDF(216 KB) | IEEE CNF 70. Optimization of phase-locked loop circuits via geometric programming Colleran, D.M.; Portmann, C.; Hassibi, A.; Crusius, C.; Mohan, S.S.; Boyd, S.; Mar Hershenson, M.; Custom Integrated Circuits Conference, 2003. Proceedings of the IEEE 2003 21-24 Sept. 2003 Page(s):377 - 380 Digital Object Identifier 10.1109/CICC.2003.1249422 AbstractPlus | Full Text: PDF(355 KB) IEEE CNF 71. QoS in ad hoc networks Pandya, A.; Pottie, G.; Vehicular Technology Conference, 2003. VTC 2003-Fall. 2003 IEEE 58th Volume 5, 6-9 Oct. 2003 Page(s):3089 - 3093 Vol.5 Digital Object Identifier 10.1109/VETECF.2003.1286192 AbstractPlus | Full Text: PDF(258 KB) | IEEE CNF 72. Efficient description of the design space of analog circuits \Box Hershenson, Md.M.; Design Automation Conference, 2003. Proceedings 2-6 June 2003 Page(s):970 - 973 AbstractPlus | Full Text: PDF(471 KB) | IEEE CNF 73. Generalized posynomial performance modeling [analog ICs] Eeckelaert, T.; Daems, W.; Gielen, G.; Sansen, W.; Design, Automation and Test in Europe Conference and Exhibition, 2003 2003 Page(s):250 - 255 Digital Object Identifier 10.1109/DATE.2003.1186394 AbstractPlus | Full Text: PDF(320 KB) | IEEE CNF 74. Reverse geometric programming with fuzzy coefficient and its application industry production cost analysis Cao Bing-yuan; Fuzzy Systems, 2003. FUZZ '03. The 12th IEEE International Conference on Volume 2, 25-28 May 2003 Page(s):1311 - 1316 vol.2 Digital Object Identifier 10.1109/FUZZ.2003.1206620 AbstractPlus | Full Text: PDF(324 KB) | IEEE CNF 75. Creating electrical distribution boundaries using computational geometry Held, M.; Williamson, R.B.; Power Systems, IEEE Transactions on Volume 19, Issue 3, Aug. 2004 Page(s):1342 - 1347 Digital Object Identifier 10.1109/TPWRS.2004.831244 AbstractPlus | References | Full Text: PDF(216 KB) | IEEE JNL 76. Geometric programming duals of channel capacity and rate distortion П Mung Chiang; Boyd, S.; Information Theory, IEEE Transactions on Volume 50, Issue 2, Feb. 2004 Page(s):245 - 258 Digital Object Identifier 10.1109/TIT.2003.822581 AbstractPlus | References | Full Text: PDF(344 KB) | IEEE JNL 77. Improved Routh-Pade/spl acute/ approximants: a computer-aided approa Singh, V.; Chandra, D.; Kar, H.; Automatic Control, IEEE Transactions on

Volume 49, Issue 2, Feb. 2004 Page(s):292 - 296

Digital Object Identifier 10.1109/TAC.2003.822878 AbstractPlus | References | Full Text: PDF(176 KB) | IEEE JNL 78. Geometric programming with trapezoidal fuzzy variables \Box Cao Bing-yuan; Fuzzy Information, 2004. Processing NAFIPS '04. IEEE Annual Meeting of the Volume 2, 27-30 June 2004 Page(s):826 - 831 Vol.2 Digital Object Identifier 10.1109/NAFIPS.2004.1337409 AbstractPlus | Full Text: PDF(448 KB) | IEEE CNF 79. CMOS analog circuit design via geometric programming del Mar Hershenson, M.; American Control Conference, 2004. Proceedings of the 2004 Volume 4, 30 June-2 July 2004 Page(s):3266 - 3271 vol.4 AbstractPlus | Full Text: PDF(676 KB) | IEEE CNF 80. Techniques for improving the accuracy of geometric-programming based П design optimization Jintae Kim; Jaeseo Lee; Vandenberghe, L.; Chih-Kong Ken Yang; Computer Aided Design, 2004. ICCAD-2004. IEEE/ACM International Confere 7-11 Nov. 2004 Page(s):863 - 870 AbstractPlus | Full Text: PDF(787 KB) | IEEE CNF 81. Comparison of optimal design of different spiral inductors П Stojanovic, G.; Zivanov, L.; Microelectronics, 2004. 24th International Conference on Volume 2, 16-19 May 2004 Page(s):613 - 616 vol.2 AbstractPlus | Full Text: PDF(342 KB) | IEEE CNF 82. A Geometric Programming Framework for Optimal Multi-Level Tiling П Renganarayana, L.; Rajopadhye, S.; Supercomputing, 2004. Proceedings of the ACM/IEEE SC2004 Conference 2004 Page(s):18 - 18 Digital Object Identifier 10.1109/SC.2004.3 AbstractPlus | Full Text: PDF(520 KB) | IEEE CNF 83. Geometric programming relaxations for linear system reachability Yazarel, H.; Pappas, G.J.; American Control Conference, 2004. Proceedings of the 2004 Volume 1, 30 June-2 July 2004 Page(s):553 - 559 vol.1 AbstractPlus | Full Text: PDF(764 KB) | IEEE CNF 84. Automated design of operational transconductance amplifiers using reve programming Vanderhaegen, J.P.; Brodersen, R.W.; Design Automation Conference, 2004. Proceedings. 41st 2004 Page(s):133 - 138 AbstractPlus | Full Text: PDF(467 KB) IEEE CNF 85. Robust analog/RF circuit design with projection-based posynomial mode П Xin Li; Gopalakrishnan, P.; Yang Xu; Pileggi, T.; Computer Aided Design, 2004. ICCAD-2004. IEEE/ACM International Confere 7-11 Nov. 2004 Page(s):855 - 862 AbstractPlus | Full Text: PDF(884 KB) | IEEE CNF ^{86.} Application of Geometric Programming to Transformer Design Jabr, R.A.;

Volume 41, Issue 11, Nov. 2005 Page(s):4261 - 4269 Digital Object Identifier 10.1109/TMAG.2005.856921 AbstractPlus | Full Text: PDF(352 KB) IEEE JNL 87. Optimal maintenance policies under different operational schedules Shaomin Wu; Clements-Croome, D.; Reliability, IEEE Transactions on Volume 54, Issue 2, June 2005 Page(s):338 - 346 Digital Object Identifier 10.1109/TR.2005.847255 AbstractPlus | References | Full Text: PDF(424 KB) | IEEE JNL 88. A Cross-Layer Optimization Framework for Multicast in Multi-hop Wireles Jun Yuan; Zongpeng Li; Wei Yu; Baochun Li; Wireless Internet, 2005. Proceedings. First International Conference on 10-15 July 2005 Page(s):47 - 54 Digital Object Identifier 10.1109/WICON.2005.2 AbstractPlus | Full Text: PDF(376 KB) IEEE CNF 89. Geometric Programming with Fuzzy Relation Equation Constraints Ji-hui Yang; Bing-yuan Cao; Fuzzy Systems, 2005. FUZZ '05. The 14th IEEE International Conference on May 22-25, 2005 Page(s):557 - 560 AbstractPlus | Full Text: PDF(1849 KB) IEEE CNF 90. A new method for design of robust digital circuits П Patil, D.; Yun, S.; Kim, S.-J.; Cheung, A.; Horowitz, M.; Boyd, S.; Quality of Electronic Design, 2005. ISQED 2005. Sixth International Symposium 21-23 March 2005 Page(s):676 - 681 Digital Object Identifier 10.1109/ISQED.2005.11 AbstractPlus | Full Text: PDF(432 KB) | IEEE CNF 91. Power control in lognormal fading wireless channels with uptime probab specifications via robust geometric programming Kan-Lin Hsiung; Seung-Jean Kim; Boyd, S.; American Control Conference, 2005. Proceedings of the 2005 8-10 June 2005 Page(s):3955 - 3959 vol. 6 Digital Object Identifier 10.1109/ACC.2005.1470594 AbstractPlus | Full Text: PDF(289 KB) | IEEE CNF 92. Leakage minimization of nano-scale circuits in the presence of systemati П variations Bhardwaj, S.; Vrudhula, S.B.K.; Design Automation Conference, 2005. Proceedings. 42nd 13-17 June 2005 Page(s):541 - 546 AbstractPlus | Full Text: PDF(393 KB) | IEEE CNF 93. Robust gate sizing by geometric programming Singh, J.; Nookala, V.; Zhi-Quan Luo; Sapatnekar, S.; Design Automation Conference, 2005. Proceedings. 42nd 13-17 June 2005 Page(s):315 - 320 AbstractPlus | Full Text: PDF(391 KB) | IEEE CNF 94. OPERA: optimization with ellipsoidal uncertainty for robust analog IC de: Г Yang Xu; Xin Li; Kan-Lin Hsiung; Boyd, S.; Nausieda, I.; Design Automation Conference, 2005. Proceedings. 42nd 13-17 June 2005 Page(s):632 - 637

Magnetics, IEEE Transactions on